#### TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type

# **2SJ338**

#### **Audio-Frequency Power Amplifier Applications**

High breakdown voltage : V<sub>DSS</sub> = −180 V
 High forward transfer admittance : |Y<sub>fS</sub>| = 0.7 S (typ.)

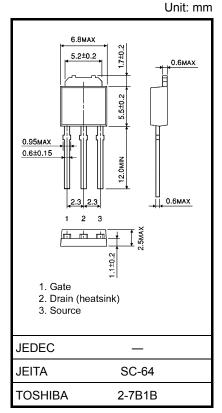
Complementary to 2SK2162

## Absolute Maximum Ratings (Ta = 25°C)

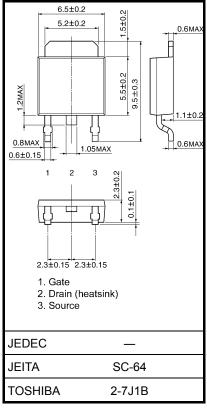
Characteristic	Symbol	Rating	Unit	
Drain-source voltage	$V_{DSS}$	-180	V	
Gate-source voltage	$V_{GSS}$	±20	V	
Drain current (Note 1)	I <sub>D</sub>	-1	Α	
Power dissipation (Tc = 25°C)	$P_{D}$	20	W	
Channel temperature	T <sub>ch</sub>	150	°C	
Storage temperature range	T <sub>stg</sub>	-55~150	°C	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 0.36 g (typ.)



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## **Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0, V <sub>GS</sub> = ±20 V	_	_	±100	nA
Drain-source breakdown voltage	V (BR) DSS	I <sub>D</sub> = -10 mA, V <sub>GS</sub> = 0	-180	_	_	V
Gate-source cutoff voltage (Note 3)	V <sub>GS (OFF)</sub>	$V_{DS} = -10 \text{ V}, I_D = -10 \text{ mA}$	-0.8	-	-2.8	٧
Drain-source saturation voltage	V <sub>DS</sub> (ON)	$I_D = -0.6 \text{ A}, V_{GS} = -10 \text{ V}$	_	-1.2	-3.0	V
Forward transfer admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_D = -0.3 \text{ A}$	_	0.7	_	S
Input capacitance	C <sub>iss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	210	_	
Output capacitance	C <sub>oss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	90	_	pF
Reverse transfer capacitance	Q <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	45	_	

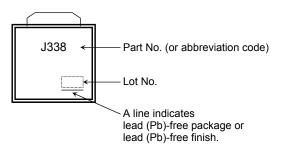
Note 3:  $V_{GS\ (OFF)}$  Classification

O: -0.8~-1.6, Y: -1.4~-2.8

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This transistor is an electrostatic-sensitive device. Handle with care.

### Marking



#### **RESTRICTIONS ON PRODUCT USE**

20070701-EN

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